

Bio:

Dr. Shubham Sahay is an assistant professor in the department of Electrical Engineering at Indian Institute of Technology (I.I.T.) Kanpur, India. Prior to joining I.I.T. Kanpur, he worked as a postdoctoral research scholar at the University of California, Santa Barbara. He received the B.Tech (Hons.) in Electronics Engineering with four gold medals and several cash prizes from the I.I.T. BHU Varanasi in 2014 and the Ph.D. degree in Electrical Engineering from I.I.T. Delhi in 2018. He has authored a book on “Junctionless Field Effect Transistors: Design, Modeling and Simulation” which is published by the Wiley-IEEE press. He has also published several peer-reviewed articles on topics including semiconductor device design and modeling, neuromorphic computing and hardware security primitives utilizing emerging non-volatile memories. He is an editor of IETE Technical Review and review editor for Frontiers in Neuroscience: Neuromorphic Engineering and Frontiers in Electronics: Integrated Circuits and VLSI. He is a member of IEEE and the vice-chair of IEEE EDS UP section. He also appeared in the list of Golden reviewers for IEEE Transactions on Electron Devices and IEEE Electron Device Letters from 2016-2020.