



Dr. Suman Lata Tripathi

FIETE, SMIEEE, LMISC

Country: India

Email: tri.suman78@gmail.com

Official Email: suman.21067@lpu.co.in

Affiliation: Lovely Professional University University, Phagwara, Punjab-144411

Designation/Department: Professor, School of Electronics and Electrical Engineering.

Specialization: Microelectronics and VLSI Design

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Dr. Suman Lata Tripathi has completed her Ph.D. in the area of microelectronics and VLSI from MNNIT, Allahabad. She did her M.Tech in Electronics Engineering from UP Technical University, Lucknow and B.Tech in Electrical Engineering from Purvanchal University, Jaunpur. She is also a remote post-doc researcher at Nottingham Trent University, London, UK in the year 2022. She is associated with Lovely Professional University as a Professor with more than 19 years of experience in academics. She has published more than 74 research papers in refereed IEEE, Springer, Elsevier and IOP science journals and conferences. She has also published 13 Indian patents and 2 copyright. She has organized several workshops, summer internships, and expert lectures for students. She has worked as a session chair, conference steering committee member, editorial board member, and peer reviewer in international/national IEEE, Springer, Wiley etc Journal and conferences. She has received the **“Research Excellence Award”** in **2019** and **“Research Appreciation Award”** in **2020, 2021** at Lovely Professional University, India. She had received the best paper at IEEE ICICS-2018. She has also received funded project **from SERB DST under the scheme TARE** in the area of Microelectronics devices. She has edited and authored more than 15 books in different areas of Electronics and electrical engineering. She is associated for editing work with top publishers like Elsevier, CRC Taylor and Francis, Wiley-IEEE, SP Wiley, Nova Science and Apple academic press etc. She is also working as book series editor for title, *“Smart Engineering Systems”* CRC Press, *“Engineering system design for sustainable developments”* Wiley-Scrivener and conference series editor for *“Conference Proceedings Series on Intelligent systems for Engineering designs”* CRC Press Taylor & Francis. She is the guest editor of a special issue in *“Current Medical Imaging”* Bentham Science. She is associated as **senior member IEEE, Fellow IETE and Life member ISC** and is continuously involved in different professional activities along with academic work. Her area of expertise includes microelectronics device modeling and characterization, low power VLSI circuit design, VLSI design of testing, and advanced FET design for IoT, Embedded System Design, reconfigurable architecture with FPGAs and biomedical applications etc.