



**Dr. Aminul Islam** is a Senior Member of IEEE since 2015, and corporate member of Institute of Engineers (India). Until November 2006, he was with Indian Air Force. Since November 2006, he has been in the Department of ECE, Birla Institute of Technology, where he is currently an Associate

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His broad research area is VLSI, Microelectronics, nanoelectronics and spintronics covering a wide spectrum of memory, digital, analog and RF and mixed-signal circuit design. Research interests include VLSI Design/CAD for nanoscale silicon and non-silicon technologies which center around the emerging nanoelectronics and spintronics devices and circuits like, SiC U-MOSFET, SiC V-MOSFET, VDMOSFET, FinFET, CNFET, HEMT, SRAM, MRAM, RRAM, PCRAM, power- and variability-aware design, RF device modeling and circuit design, design of ultralow-power nanoscale circuits for portable/ wearable/ energy-harvesting applications. He has authored/coauthored more than 294 publications on refereed journals (116), conferences (135) and chapters in Springer/Elsevier book (43) including 19 research papers in IEEE transactions. He has applied for four patents, out of which two has been published.

Dr. Islam was a recipient of the National Scholarship (Govt. of India) for three years during study in high school

in 1983. He received the best paper award ten times in international conferences. He received the IET Premium Best Paper Awards 2021 (awarded in December 2021) for paper titled “Transmission gate-based 9T1SRAM cell for variation resilient low power and reliable internet of things applications”, Volume 13, Issue 5, August 2019, p. 584 – 595” from The Institution of Engineering and Technology. He is also recipient of the IET Premium Best Paper Awards 2020 for paper titled “Design and development of memristor-based RRAM”, Volume 13, Issue 4, July 2019, p. 548 – 557” and Distinguished Alumni Award from IEI Alumni Association, at The Institution of Engineers (India), West Bengal State Centre on 17<sup>th</sup> December 2017. He is a member of board of editors of various journals. He is reviewer of various international journals such as IEEE Transactions on Circuits and Systems I: Regular Papers, IEEE Transactions on Device and Materials Reliability, IEEE Transactions on Magnetics. IEEE Transactions on VLSI Systems, IEEE Access, IET Circuits, Devices & Systems, etc. He has supervised/supervising 60 UG students, 18 PG students and 08 PhD scholars. He has delivered invited talk/tutorial speech/expert talk in various International Conference /technical programs. He has organized IEEE international conference/workshop. He was granted a research project titled “Study of HEMT and its performance in amplifier circuit” of worth Rs. 23.742 lakhs by DRDO, Govt. of India. He is also working on a project titled “BCI enabled electric wheelchair control for neurorehabilitation” as CO-PI funded by DST under the scheme National Mission on Interdisciplinary Cyber-Physical Systems (NM-ICPS).