



Call For Participation

2nd Edition

National Seminar

on

New Trends in Signal Processing
(NeTSiP-2016)

20th-21st October 2016- Surat



(TEQIP-II)

Patron

Director, SVNIT
Principal, SCET

General Chair:

Suman Mitra, DA-IICT

Steering Committee

Anil Roy, DA-IICT
Deepak Mathur, ONGC
Gaurav Sharma, Univ. of
Rochester

Sanjay Chaudhary, AU

Y.P.Kosta, MEFGI

Program Co-Chair:

Chirag Paunwala, SCET
Jignesh Sarvaiya, SVNIT

Organizing Co-Chair:

Anand Darji, SVNIT
Maulin Joshi, SCET

Finance Chair:

Mukesh Goswami, DDU

Advertisement

Ankit Dave –Infosys

Mrugesh Patel- SCET

Web Committee:

Bhaumik Vaidya, SCET

Pritesh Saxena, SCET

Organizing Committee:

Aarohi Vora, SCET
Bhargav Shah, SCET
Chintan Panchal, SCET
Dhaval Patel, AU
Dhiren Bhagat, SCET
Jigisha Patel, SVNIT
Ketki Pathak, SCET
Keyur Rana, SCET
Mehul Raval, AU
Mita Parikh, SCET
Mita Paunwala, CKPCET
Mukesh Zaveri, SVNIT
Mustafa Surti, SCET
Naresh Patel, SCET
Neeta Chapatwala, SCET
Nehal Shah, SCET
Niteen Patel, SCET
Pariza Kamboj, SCET
Pranav Lapsiwala, SCET
Rahul Kher, GCET
S.V. Patel, SCET
Sarosh Dastoor, SCET
Shweta N Shah, SVNIT
Upena Dalal, SVNIT
Vishvijit Thakar, ADIT



Since the dawn of civilization, signals are part and parcel of our life and now of our lifestyles, too. Today, signal processing technologies that enrich our lives are omnipresent and embedded in not only in consumer devices such as mobile phones, TVs, automobiles, GPS navigation systems, modems, cameras, and scanners, but also in communication systems, audio equipment, and medical devices. Additionally, signal processing forms the basis of data and information analytics that power many of the services we use daily.

New developments in signal processing are providing novel ways for sensing and interacting with the world around us and with each other, helping us make sense of the increasingly large volumes of data that we can gather and access, and enabling mobility, ubiquitous connectivity and near universal access to information resources. The 2016 **National Seminar on New Trends in Signal Processing (NeTSiP-2016)** (<http://ieeegujaratsection.org/events/netsip16/>) features talks by leading researchers in signal processing that highlight these trends, spanning across the sub-fields of speech, image, audio, and video processing, biometrics, media security, bioinformatics, and communications. In addition to providing a forum for learning about new developments in signal processing. NeTSiP-2016 seeks to accelerate research and development in signal processing by creating opportunities for upcoming researchers to network and exchange ideas with established leaders and through panel discussions that highlight emerging challenges and opportunities. **Students, faculties, scientists, and engineers are invited to participate in NeTSiP-2016 to attend the presentations and to interact with the internationally acclaimed presenters.**

The plenary talk for NeTSiP-2016 will feature Prof. Yoram Bresler, professor, ECE Dept., University of Illinois, USA. Prof. Bresler is an IEEE Fellow and also on the editorial board of IEEE Journal on Selected Topics in Signal Processing. His current research interests include compressed sensing, multi-dimensional and statistical signal processing and their applications to inverse problems in imaging, and computed tomography and magnetic resonance imaging, in particular.

Prof. Tulay Adali, Distinguished Professor, CSEE Dept, UMBC will deliver a keynote address. Prof. Adali is a Fellow of the IEEE and the AIMBE, a Fulbright Scholar, recipient of 2013 University System of Maryland Regents' Award for Research, and an NSF CAREER Award.

NeTSiP-2016 is jointly organized by the IEEE India Council and Gujarat Section and will be hosted by SVNIT and SCET, Surat, Gujarat.

Speakers:

- Prof. Yoram Bresler, IEEE Fellow, University of Illinois, Urbana-Champaign, USA
- Prof. Tulay Adali, Fellow IEEE, Professor, UMBC, USA
- Prof. Suprava Patnaik, Associate Professor, Xavier Institute of Engineering, Mumbai
- Prof. Sumantra Dutta Roy, Associate Professor, IIT Delhi
- Prof. Uttama Lehari, Associate Professor, IIT Gandhinagar
- Prof. Ashvini Deshpande, Associate Professor Cummins College of Engineering, Pune
- Prof. Anand Darji, Asst. Professor, SVNIT, Surat

For Whom

Researchers from academia including faculty members, Ph.D. scholars, M.Tech. / M.E. and B.Tech. / B.E. as well as industry participants interested in the field of signal processing.

Registration Details:

| Category | IEEE members | | Non-IEEE members | |
|---------------------|--------------------------------------|---------------------------------|--------------------------------------|---------------------------------|
| | Early Bird (Before 15/10/2016) | After Early Bird Deadline | Early Bird (Before 15/10/2016) | After Early Bird Deadline |
| Student | 750 | 1000 | 1250 | 1500 |
| Professional | 1250 | 1500 | 1750 | 2000 |

Registration fees cover the food and registration kit.

#Accommodations may be provided to students in hostels on chargeable basis on prior separate request subject to availability.

Registration:

Download the registration form from the website and pay NEFT/ Demand Draft / at par Cheque in favour of "IEEE Gujarat Section", Payable at Ahmadabad, Gujarat.

Directly credit into the Account:

Bank Account Name: IEEE GUJARAT SECTION

BANK NAME: STATE BANK OF INDIA

Branch Name: SBI, IIM, AHMEDABAD BRANCH

BANK BRANCH CODE: 02653

Bank Account number: 10307643269

IFSC Code: SBIN0002653

MIRC Code: 380002019

SWIFT CODE: SBININBB412

Registration forms along with DD/Cheque should be sent

To,

Prof. Neeta Chapatwala,

Sarvajanik College of Engg and Tech, Dr. R. K. Desai Marg, Athwalines

Gujarat 395001.

Tel. (+91) 0261 – 2240146 Mo. No: +919712993433

E-mail: netsip16@scet.ac.in